

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
(Not for submission under 37 CFR 1.99)

Application Number	09989777
Filing Date	2001-11-19
First Named Inventor	Craig Nemecek
Art Unit	2123
Examiner Name	Mary C. Jacob
Attorney Docket Number	CD01208M

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Examiner Initial*	Cite No	Patent Number	Kind Code†	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
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Examiner Initial*	Cite No	Publication Number	Kind Code [†]	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
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	7	"VHDL Samples" retrieved on January 29, 2007 from http://www.csee.umbc.edu/help/VHDL/samples/samples.shtml ; 10 pages	<input type="checkbox"/>
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